

FIG.1

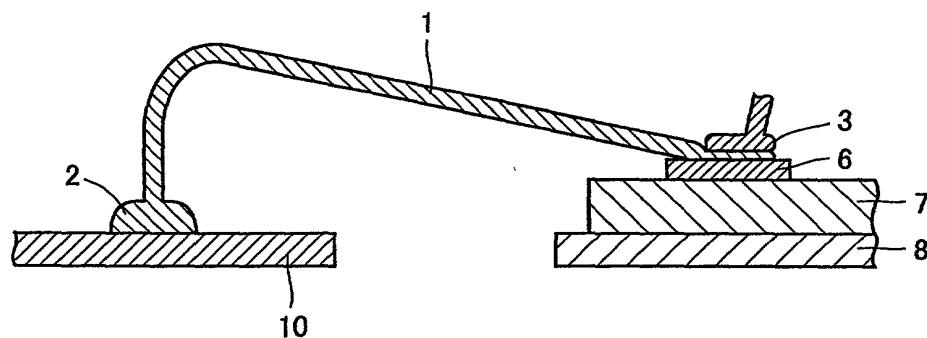
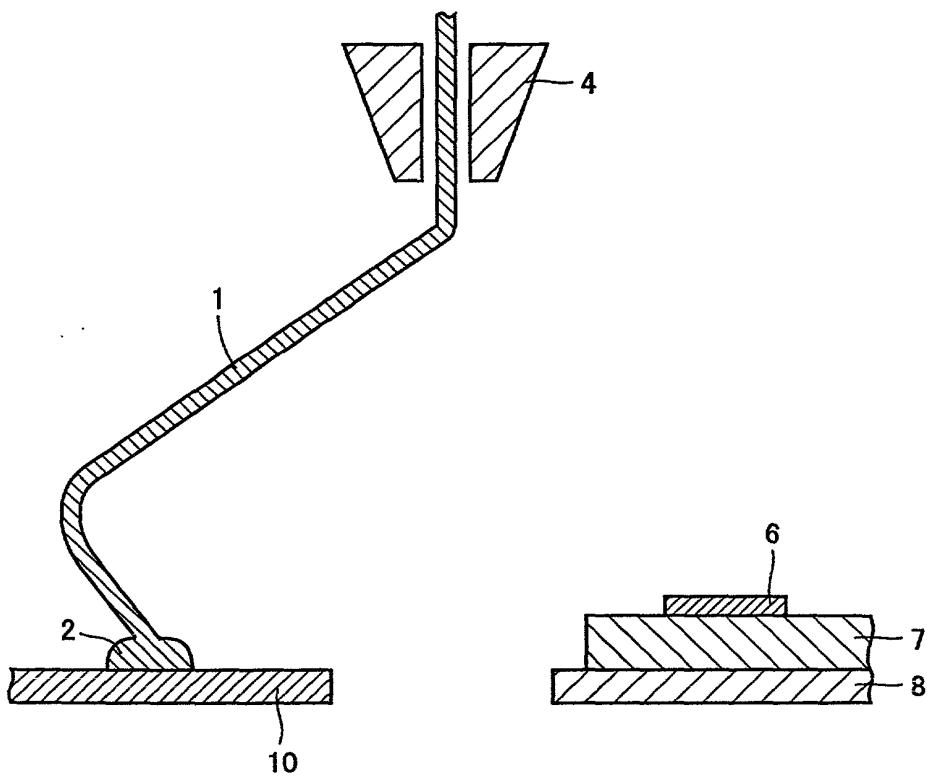


FIG.2



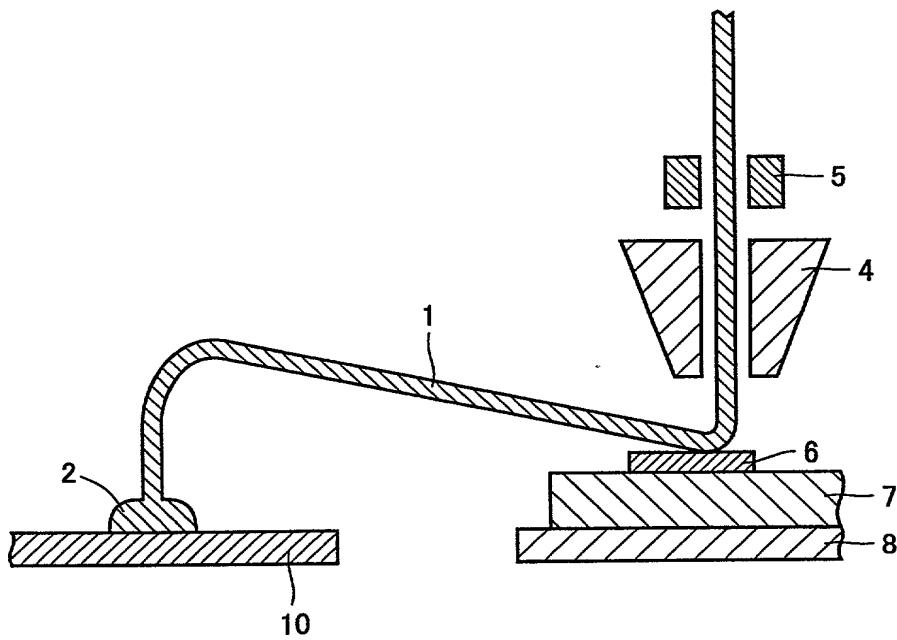
Title: Semiconductor Device and Manufacturing Method Thereof

Inventors: Hideyuki ARAKAWA

Atty Docket No.: 401346

Leydig, Voit & Mayer, Ltd. 202-737-6770

FIG.3



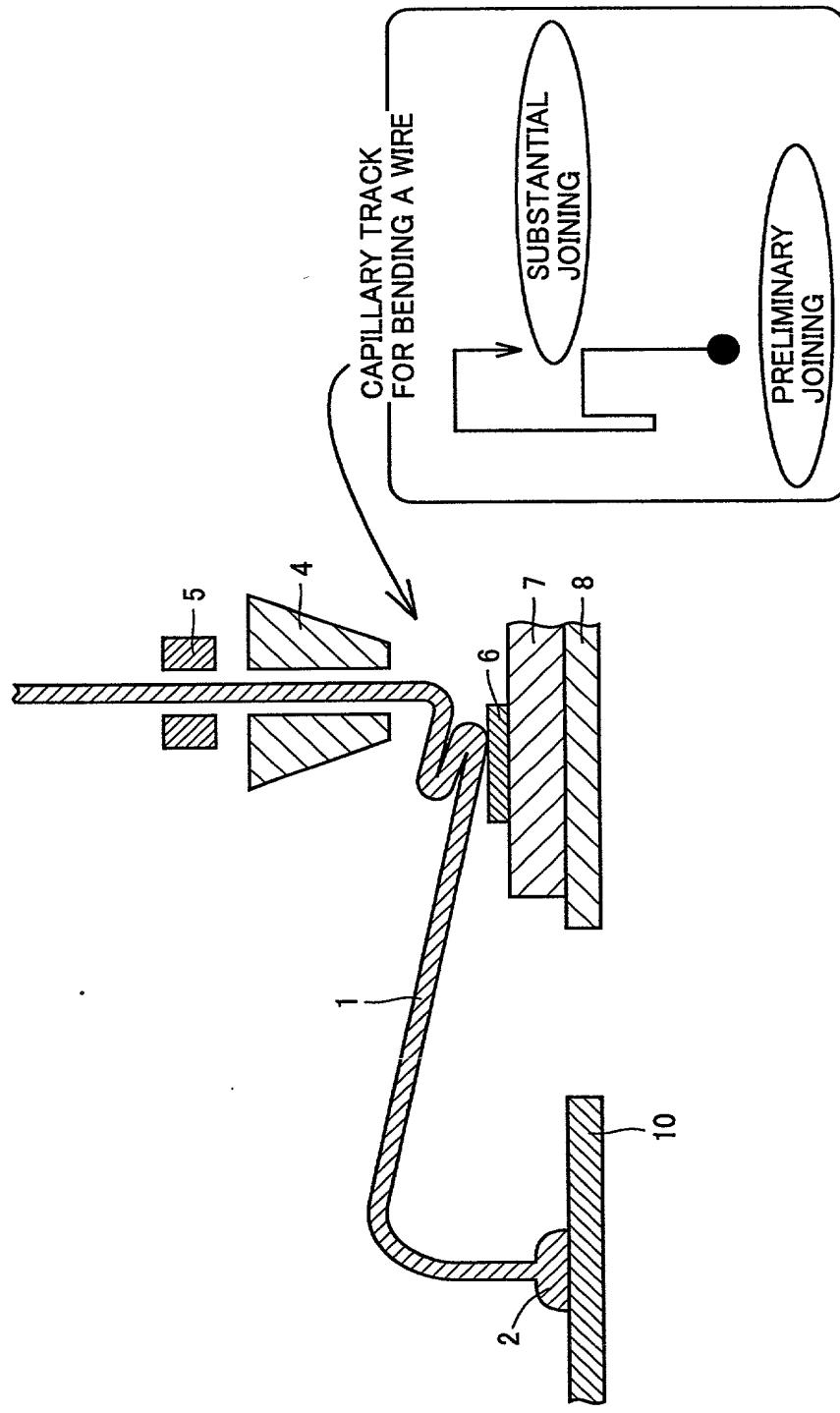
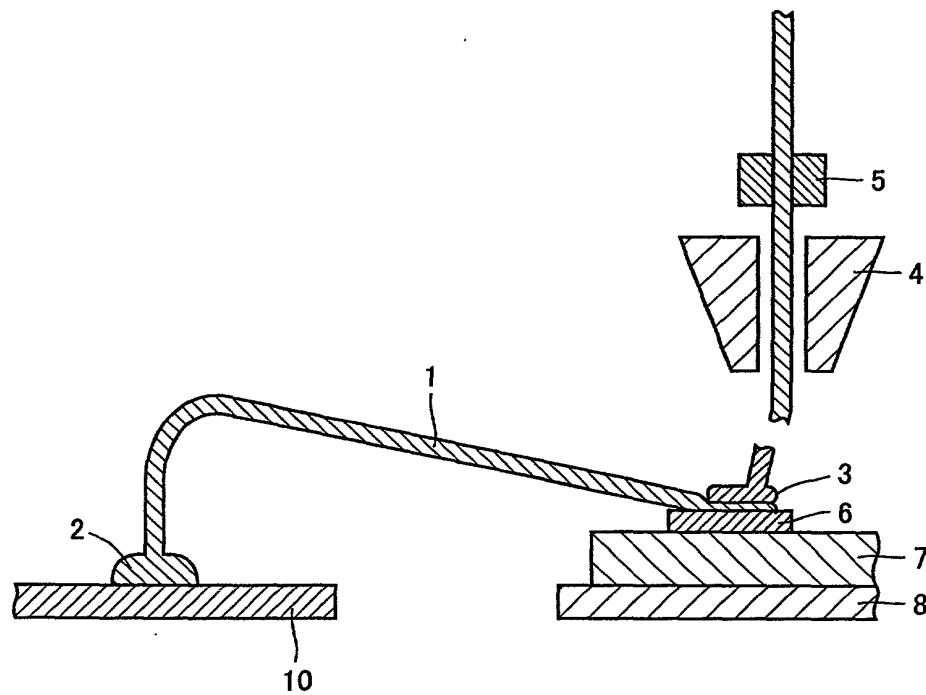


FIG. 4

FIG.5



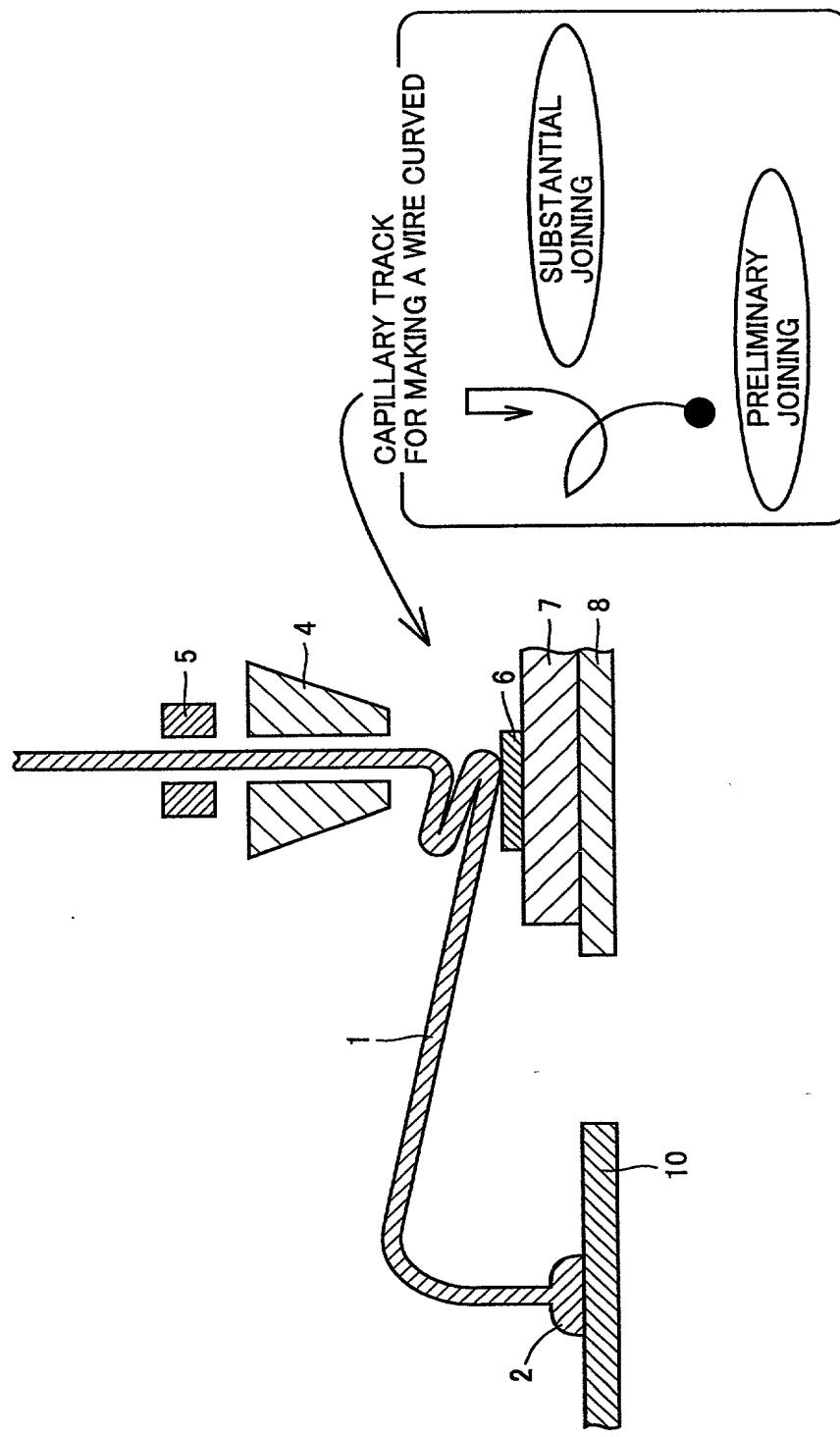


FIG.6

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Atty Docket No.: 401346

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FIG.7A

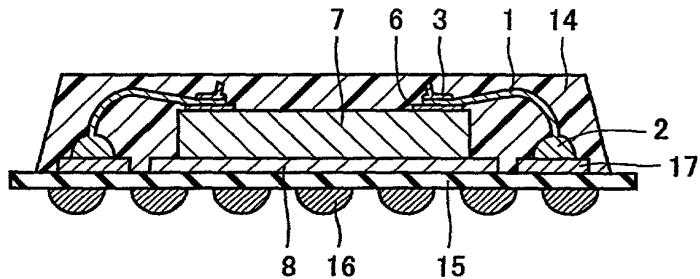


FIG.7B

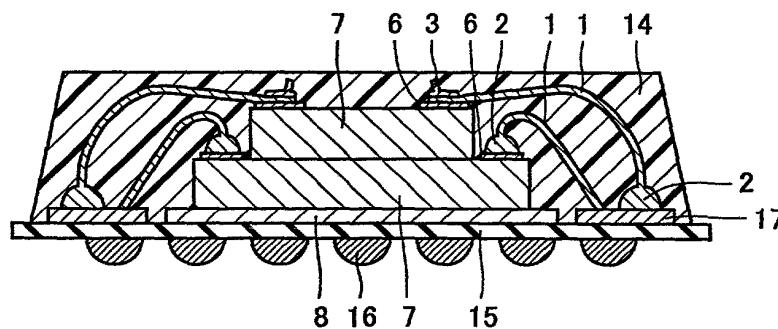


FIG.7C

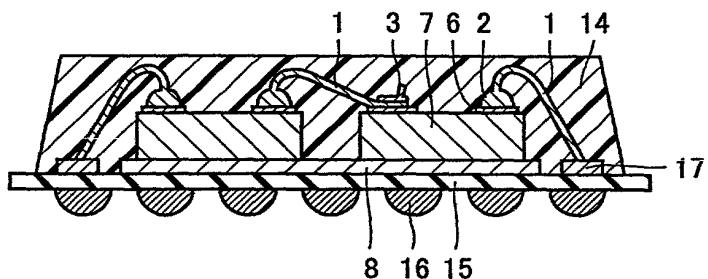


FIG.8 PRIOR ART

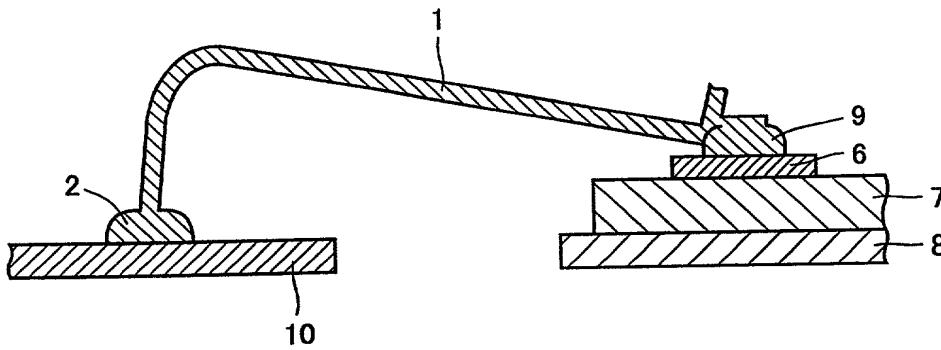


FIG.9 PRIOR ART

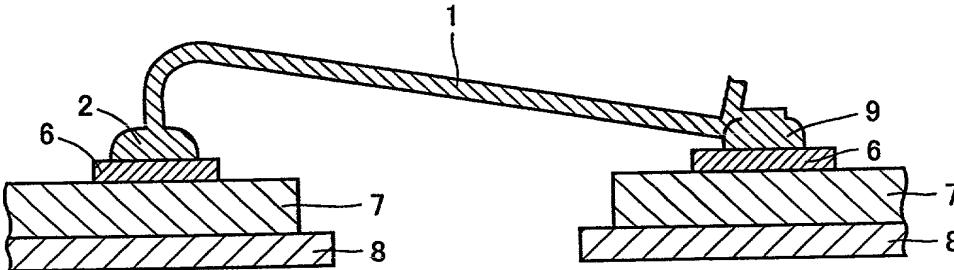


FIG.10 PRIOR ART

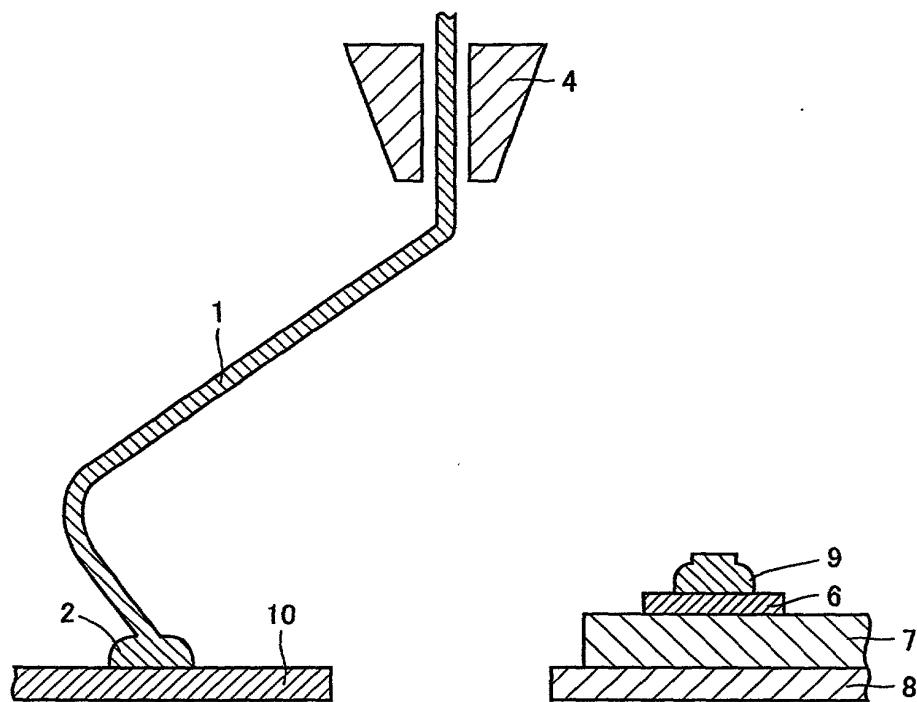


FIG.11 PRIOR ART

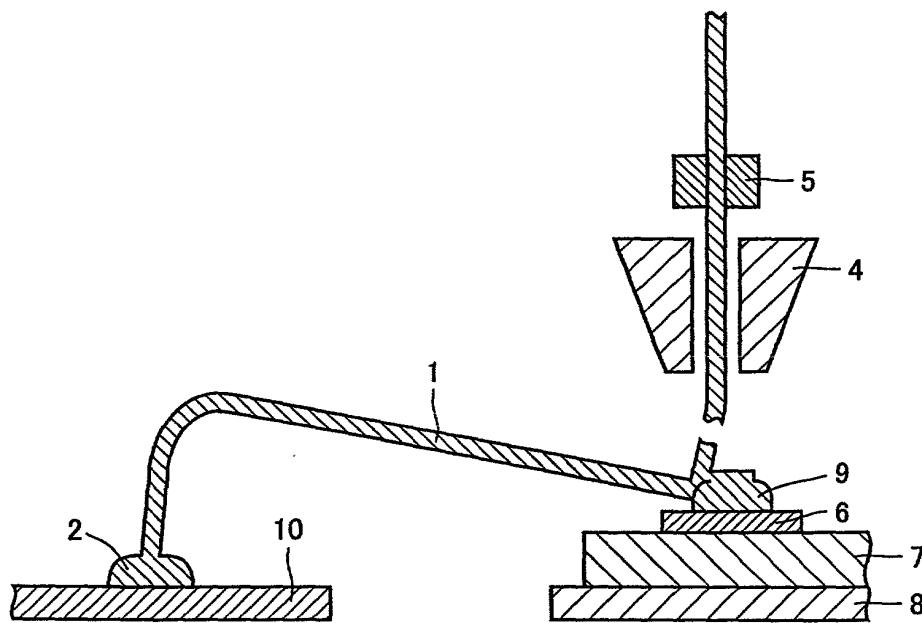


FIG.12 PRIOR ART

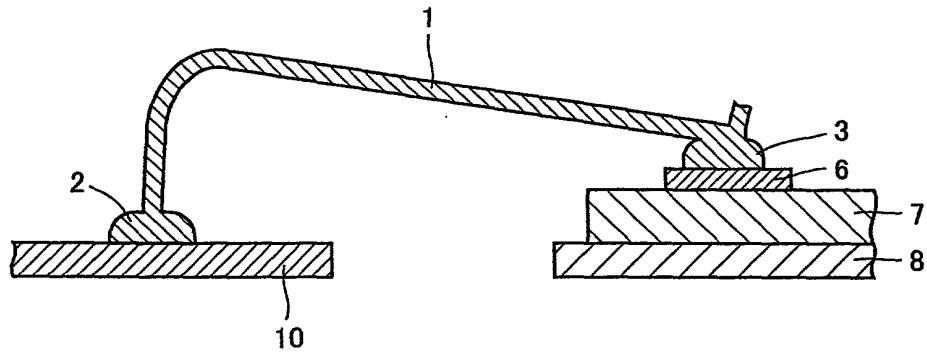


FIG.13 PRIOR ART

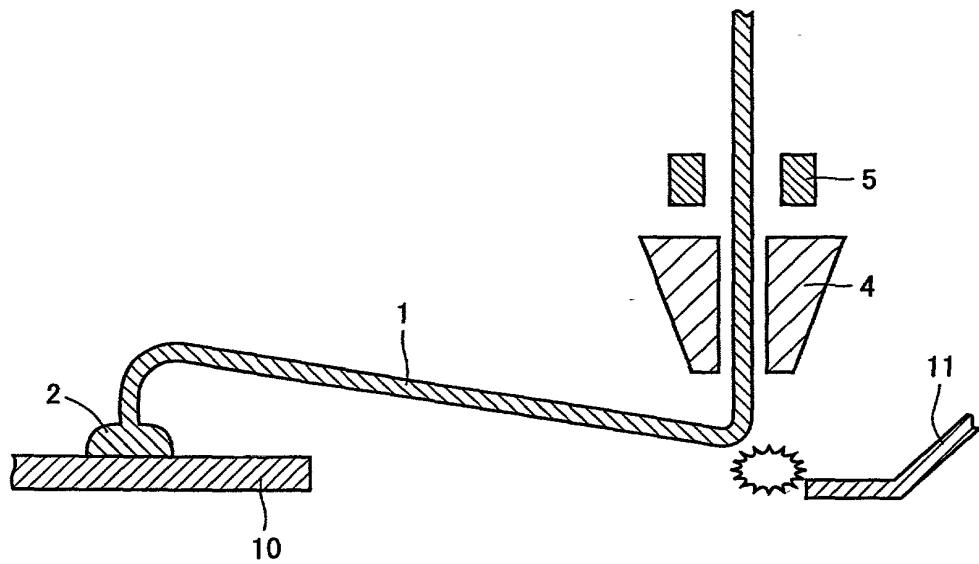


FIG.14 PRIOR ART

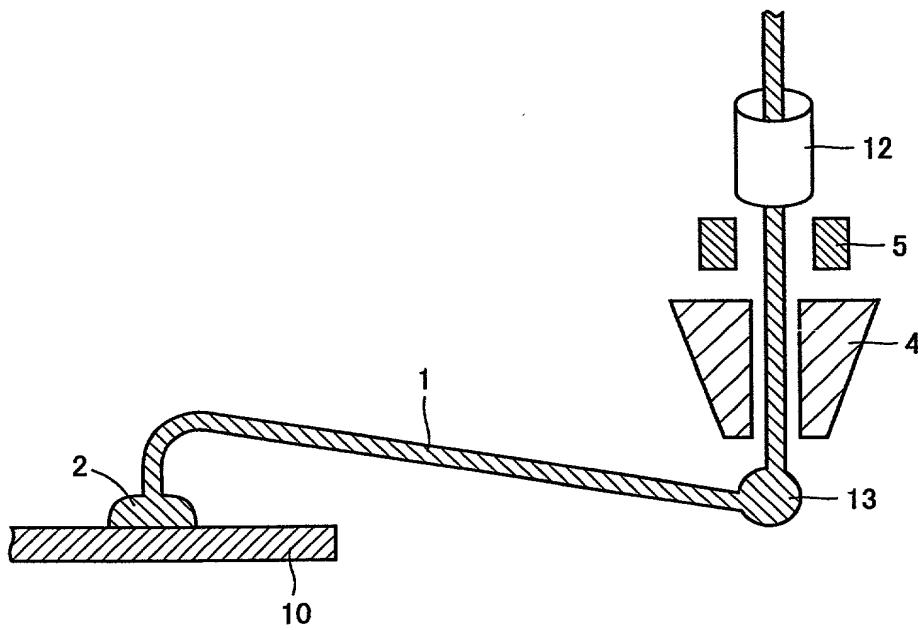


FIG.15 PRIOR ART

